

**MINUTES OF 10th MEETING
OF
BOARD OF STUDIES (BOS)**

**DEPARTMENT OF ELECTRONICS &
COMMUNICATION ENGINEERING
(INSTITUTE OF ENGINEERING &
TECHNOLOGY)**



AUGUST 05, 2017

10-08-2017

Department of Electronics & Communication Engineering

Minutes of the meeting of the Board of Studies

The meeting of the Board of Studies of Department of Electronics & Communication Engineering was held on August 05, 2017 at 11:00 AM in the office of Head, Deptt. of Electronics & Communication Engineering. Following members attended the meeting.

a) Members of the Department:

Prof. V. K. Deolia
Dr. Vishal Goyal
Dr. Atul Bansal
Mr. Abhay Chaturvedi
Mr. Aasheesh Shukla
Mr. Anjan Kumar
Mr. Diwakar Agrawal
Dr. Vinay Kumar Tomar
Dr. Bramha Prasad Pandey
Mr. P. Bachan
Mr. Manish Kumar
Mr. Paresh Chandra Sau
Mr. Devendra Kumar
Ms. Sweta
Mr. Dheeraj Kalra
Mr. Divesh Kumar
Ms. Divya Singh
Mr. Deepak Mittal
Ms. Neetu Agrawal
Mr. Puneet Mishra
Ms. Alka Agrawal

b) External experts :

Prof. H. M. Gupta
Mr. E. Sunil Kumar (Through Skype)
Prof. Ashish Sharma

c) Member from other Department:

Agenda Item-10.1: To confirm the minutes of the 9th Board of Studies meeting Studies held on May 17, 2016

The minutes of the meeting of 9th BoS held on May 17, 2016 were implemented.

Agenda Item-10.2: To report the action taken on the minutes of 9th meeting of Board of Studies held on May 17, 2016

Approved by Academic Council and implemented. No action pending.

Agenda Item-10.3: Based on the feedback received from students, faculty, employers & alumni the revision/ upgradation/ modification in syllabus of existing courses in various programmes is proposed as follows

Programme	No. of courses under consideration for revision	Name of courses under consideration for revision
B.Tech. (Electronics & Communication Engineering)	6	• Network Analysis and Synthesis, • Electronic Measurement and Instrumentation, • Digital Image Processing • Telecommunication Switching • High Speed Semiconductor Devices • Introduction to Electric Drives

Members considered and approved the same.

(Annexure-I)

Agenda Item-10.4: Course structure and syllabus of B.Tech. Electronics and Communication Engineering (ECE) based on choice based credit system (CBCS) for the batch admitted in session 2017-18 were discussed & approved as follows.

- CBCS course structure of B.Tech was discussed and approved
- List & syllabus of program core, program elective with four specialization bouquets as Communication Engineering, Signal and Image Processing, VLSI, Control and Embedded System and their syllabi were discussed and approved.
- List of project components was discussed & approved.

(Annexure-II)



Agenda Item-10.5: Based on the feedback of various stake holders (faculty, alumni, corporate and students) department presented details of new courses to be introduced. After through discussion board approves the same. (Annexure-III)

Agenda Item-10.6. To consider & approve the list of courses having focuses on Employability/Entrepreneurship/Skill Development

The members considered and approved the list of courses having focuses on Employability/Entrepreneurship/Skill Development in relevant programme.

(Annexure-IV)

Agenda Item-10.7: Following modifications in course structure and syllabi of B. Tech. Electronics and Communication Engineering were discussed & approved (For non-CBCS System)

- (d) RF Integrated Circuits (Elective) is shifted from VI Semester to VIII Semester
- (e) High speed semiconductor Devices (Elective) is shifted from Semester VIII to VI Semester

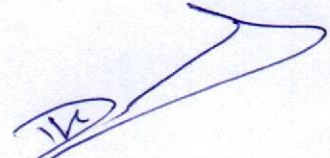
(Annexure-V)

Agenda Item-10.8: Following modifications in course structure and syllabi of the M. Tech. (Full Time) Electronics and Communication Engineering were discussed & approved.

- (a) Teaching scheme of Signal Processing Lab with LTP: 0 0 4 with credit 2 is changed to LTP: 0 0 2 with credit 1.
- (b) Teaching scheme of Communication Lab with LTP: 0 0 4 with credit 2 is changed to LTP: 0 0 2 with credit 1. This lab is shifted from Semester II to Semester I
- (c) Design and Simulation Lab with LTP: 0 0 4 with credit 4 is replaced with two Labs ie HDL Programming lab (LTP: 0 0 2 with credit 1) and RF and Microwave Design Lab (LTP: 0 0 2 with credit 1).
- (d) Minor Project with LTP: 0 0 6 with credit 3 is introduced in II Semester
- (e) Teaching scheme of Seminar is changed from (LTP: 0 0 4 with credit 2) to (LTP: 0 0 2 with credit 1)
- (f) Advanced Digital Signal Processing is renamed as Adaptive Signal Processing

(Annexure-VI)

Agenda Item-10.9: Modifications in course structure and syllabi of M. Tech (Part Time) Electronics and Communication Engineering were discussed and approved in lines with modifications mentioned in Item No.10.8. (Annexure-VII)



Prof. (Dr.) Vinay Kumar Deolia

Head, Dept. of ECE

Head of Department

Electronics & Communication Engg.

GLA University, Mathura

Encl.: As above

CC : PS to Vice-Chancellor for his kind information

Pro-Vice Chancellor

Dean, Academics Affairs

Director, IET

Director, IAH

Controller of Examination

Registrar

All members of the Board of Studies